S/N 09/510,375

IN THE UNITED STATES RA TRADEMARK OFFICE

FEB 0 6 2001

Applicant:

Brett L. Williams

Serial No.:

09/510,375

Filed:

February 22, 2000

Title:

Docket: 303.164US3 SYSTEM SUPPORTING MULTIPLE MEMORY MODES INCLUDING A

BURST EXTENDED DATA OUT MODE (AS AMENDED)

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Commissioner for Patents Washington, D.C. 20231

TECEIVED INDION COMER 2100 Applicant has reviewed the Office Action mailed on December 29, 2000. Detailed comments to the Office Action are found in the Remarks section below. Please amend the above-identified patent application as follows:

IN THE TITLE

Please replace the title with the following new title:

--System Supporting Multiple Memory Modes Including a Burst Extended Data Out Mode--.

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IN THE CLAIMS

Please amend the claims as follows:

(Once Amended) A system, comprising:

a bus for transferring information;

a memory, coupled to the bus, comprised of a memory device [having a first operation] operable in a mode selected from the group consisting of burst extended data out mode and a second operation mode, wherein the memory has a first set of access control signals for operation in the [first operation] burst extended data out mode and a second set of access control signals for operation in the second operation mode;

a programmable memory controller, coupled to the bus and to the memory, capable of providing the first set of access control signals and the second set of access control signals to the memory; and

a processor, coupled to the bus and the memory controller, responsive to at least

**PATENT** 

Examiner: Hong Kim

Group Art Unit: 2185